

501.39474X00



N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Hiroyuki NAKANO et al

Serial No.:

09/760,704

Filed:

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For:

METHOD OF PROCESSING A SEMICONDUCTOR DEVICE

Art Unit:

2812

Examiner:

Unassigned

INFORMATION DISCLOSURE STATEMENT

November 26, 2001

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In connection with the above-identified application and pursuant to Applicant's duty of disclosure, enclosed please find nine (9) documents and a list, on a form substantially equal to Form PTO-1449, listing these documents.

This Information Disclosure Statement is being submitted prior to a first Office Action on the merits in the above-identified application. Accordingly, requirements of 37 CFR 1.97(b) are satisfied.

To the extent that the enclosed documents are not in English, it is respectfully submitted that the requirements of 37 CFR 1.98(a) are satisfied by the enclosed English Abstracts.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with all documents of the enclosed list. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Please charge any shortage in fees due in connection with the filing of this paper, or credit any overpayment of fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (501.39474X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

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Attachments